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PTO/SB/21 (08-03)

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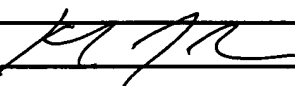
Application Number	10/614,423
Filing Date	July 7, 2003
First Named Inventor	Shrimali
Art Unit	2661
Examiner Name	Unknown
Attorney Docket Number	RAMB-01013US0

Total Number of Pages in This Submission

ENCLOSURES (Check all that apply)

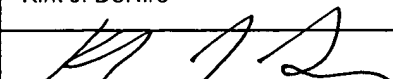
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| <input type="checkbox"/> Response to Missing Parts/Incomplete Application | | |
| <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53 | | |

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Kirk J. DeNiro, Esq., Vierra Magen Marcus Harmon & DeNiro LLP
Signature	
Date	February 18, 2004 2/18/04

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Art Unit	unknown
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Attorney Docket Number	RAMB-01013US0

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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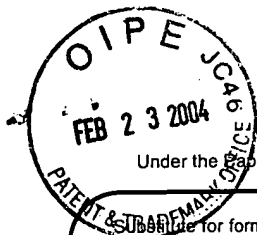
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First Named Inventor

Shrimali

Art Unit

Unknown

Examiner Name

Unknown

Attorney Docket Number

RAMB-01013US0

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		DAI & PRABHAKAR, "The throughput of data switches with and without speedup"	
		BENETT & ZHANG, "Hierarchical Packet Fair Queueing Algorithms"	
		ZHANG, TOWSLEY & KUROSE, "Statistical Analysis of the Generalized Processor Sharing Scheduling Discipline"	
		SHREEDHAR & VARGHESE, "Efficient Fair Queueing using Deficit Round Robin", November 18, 1994	
		CHI & TAMIR, "Decomposed Arbiters for Large Crossbars with Multi-Queue Input Buffers", Proceedings of the International Conference on Computer Design, Cambridge, MA, pp. 233-238, October 1991	

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